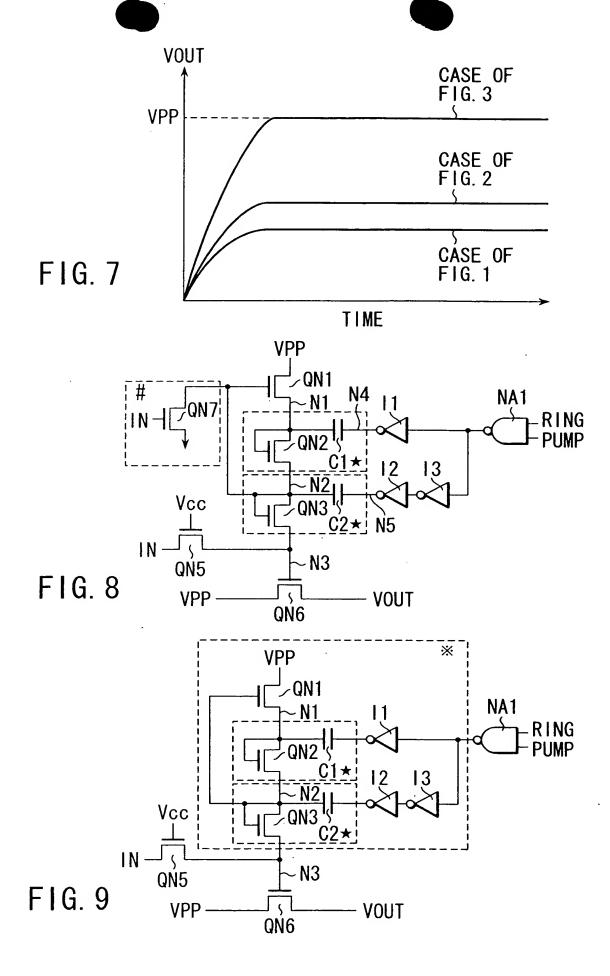
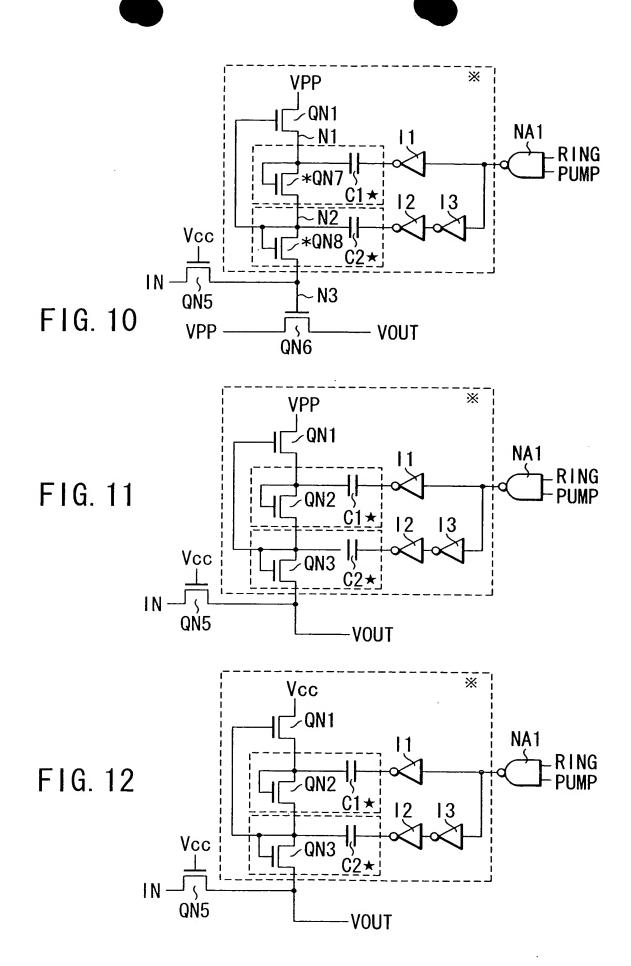
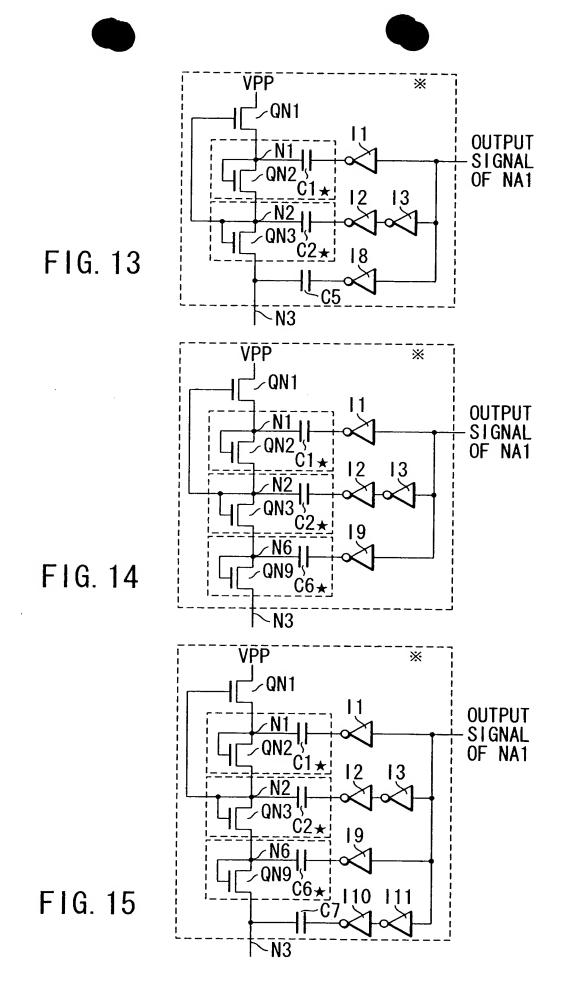


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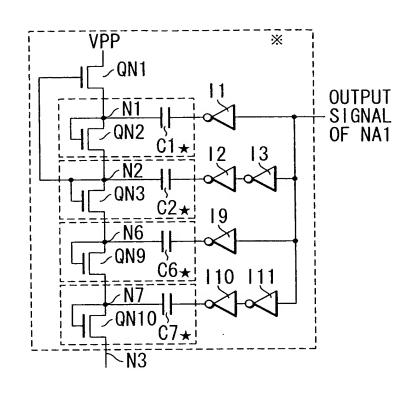


FIG. 16

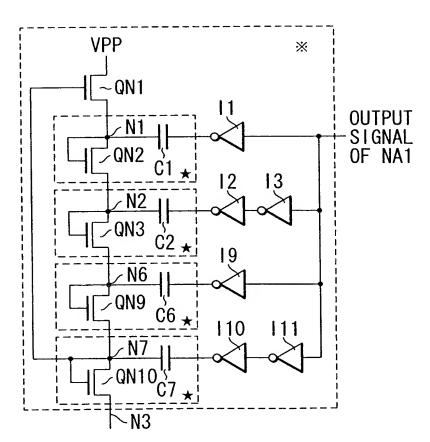
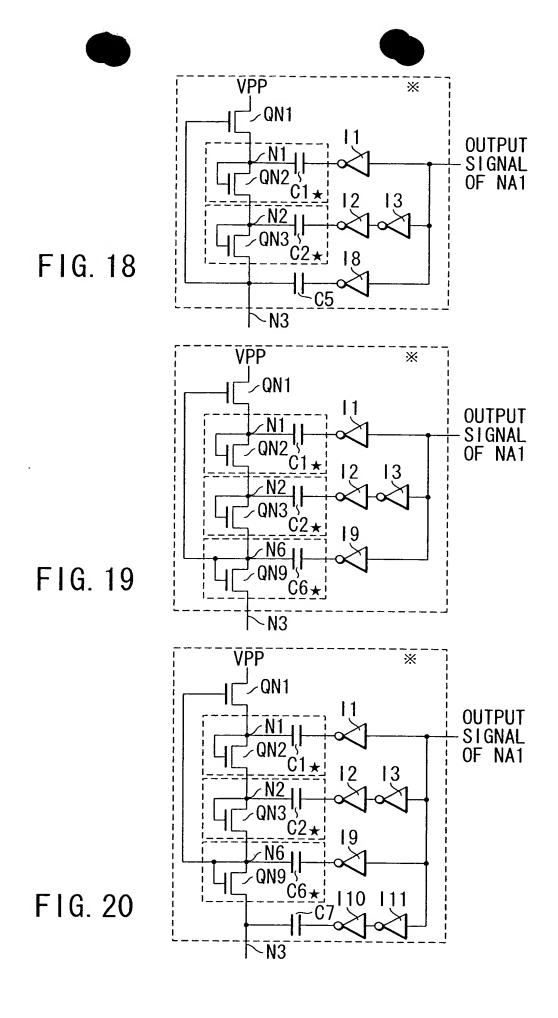


FIG. 17



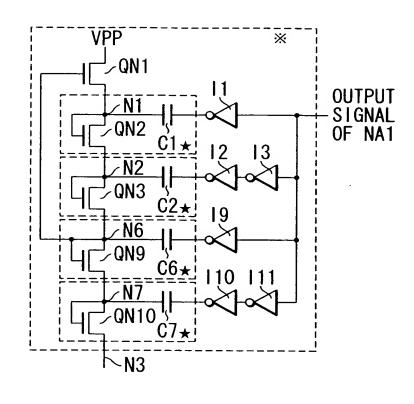


FIG. 21

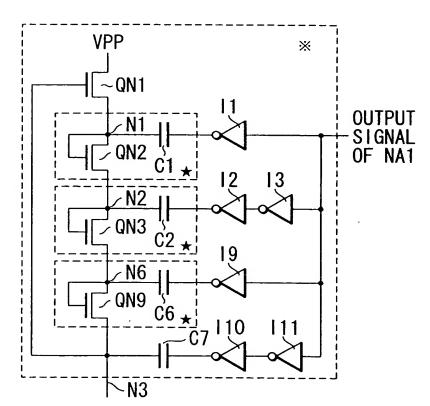
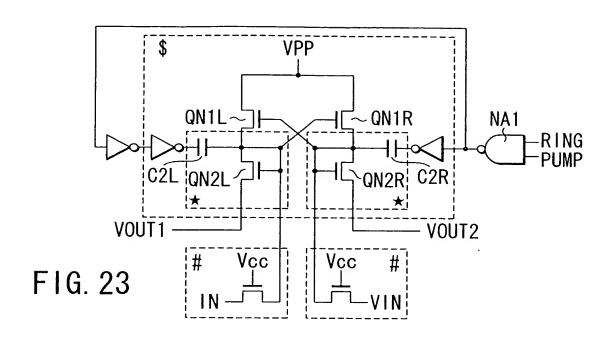
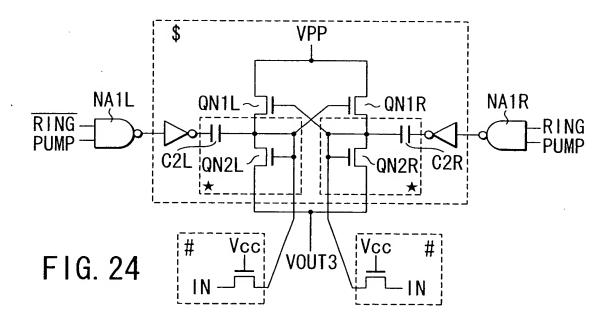
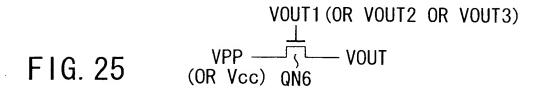
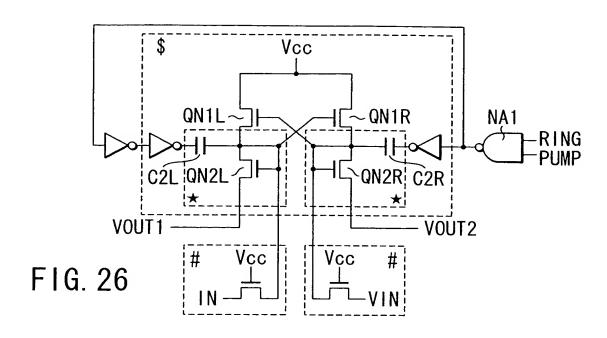


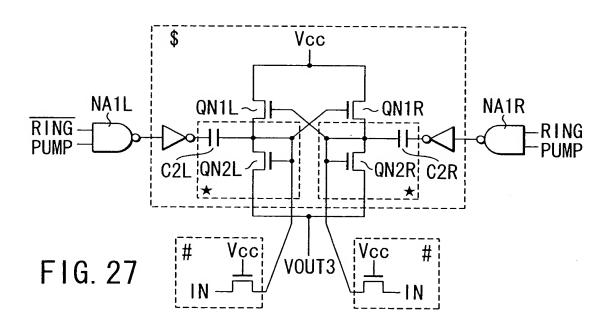
FIG. 22





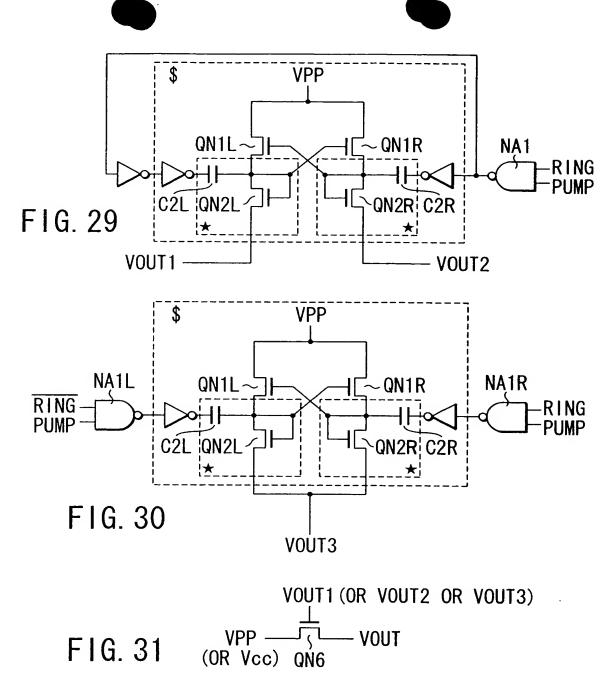


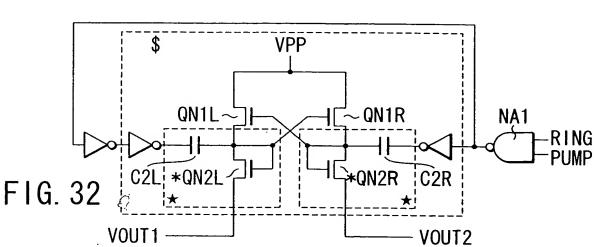


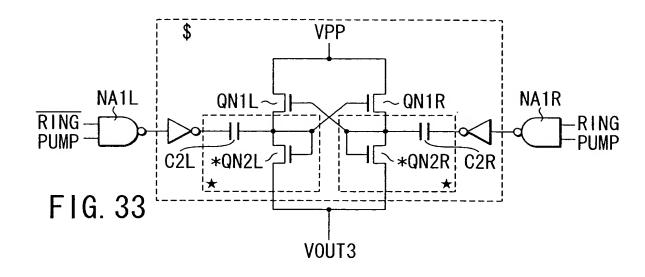


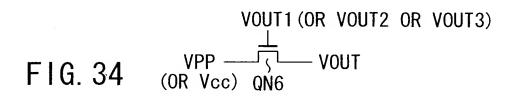
VOUT1 (OR VOUT2 OR VOUT3)

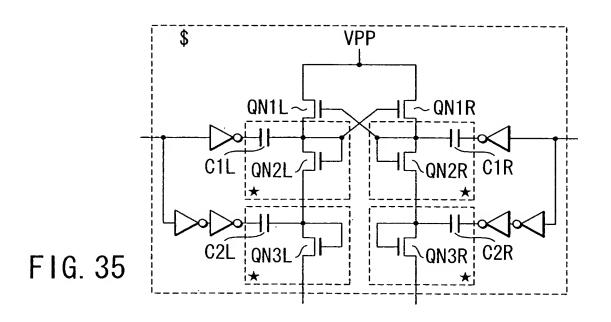
VPP VOUT
(OR Vcc) QN6

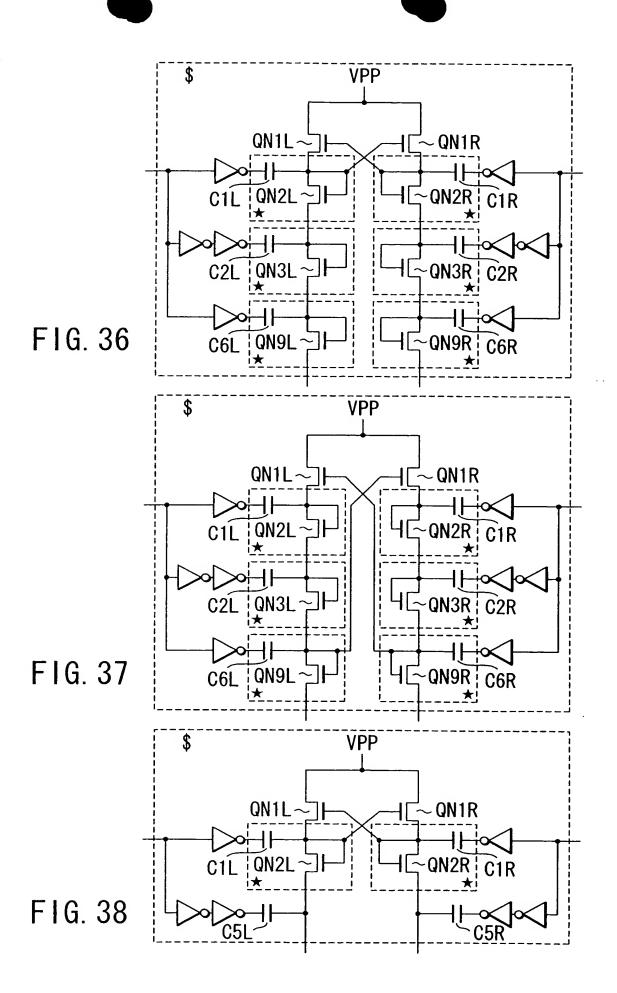


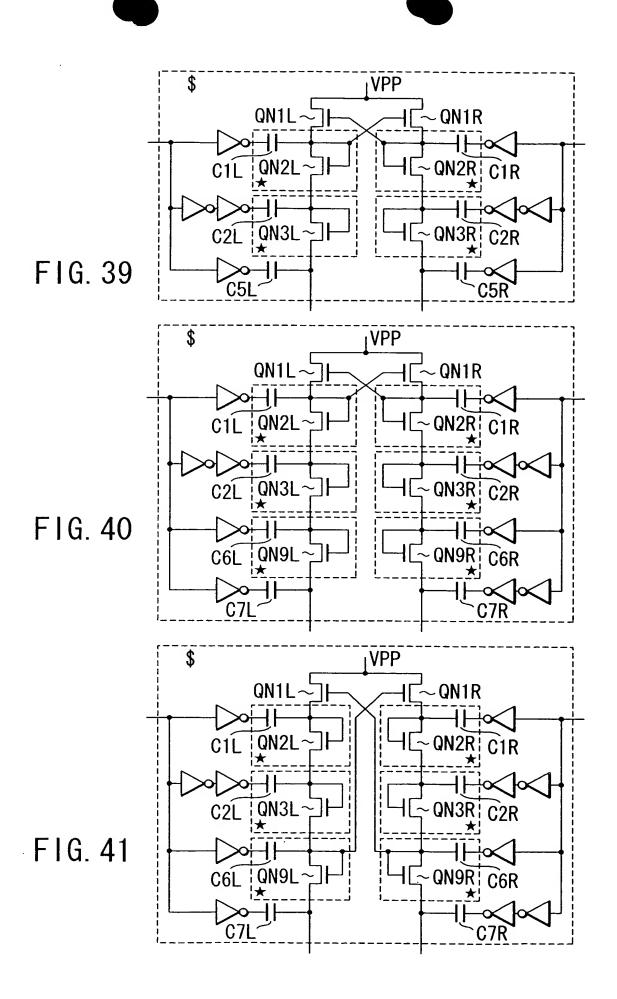




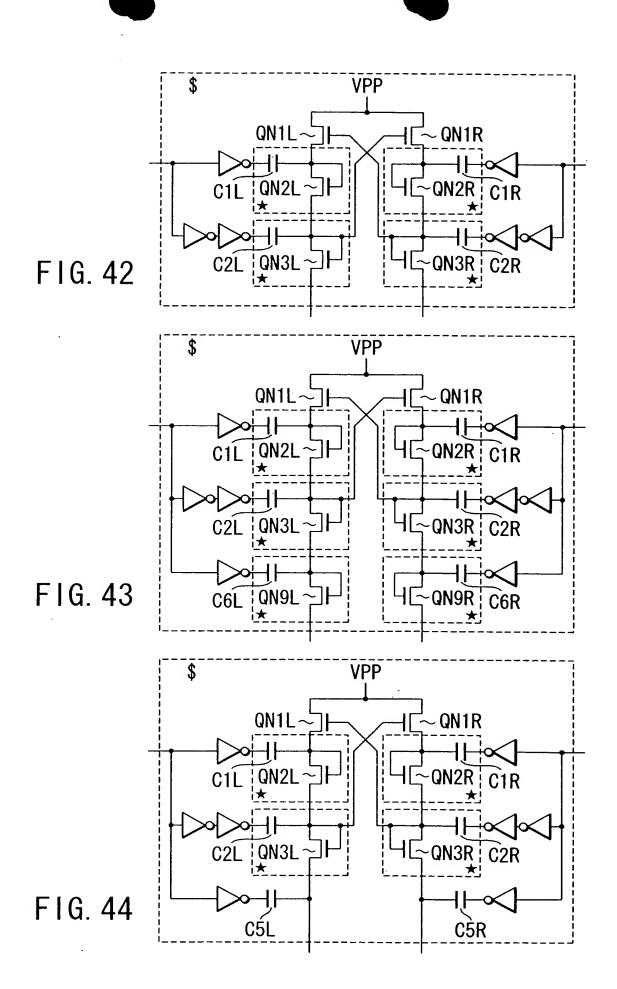


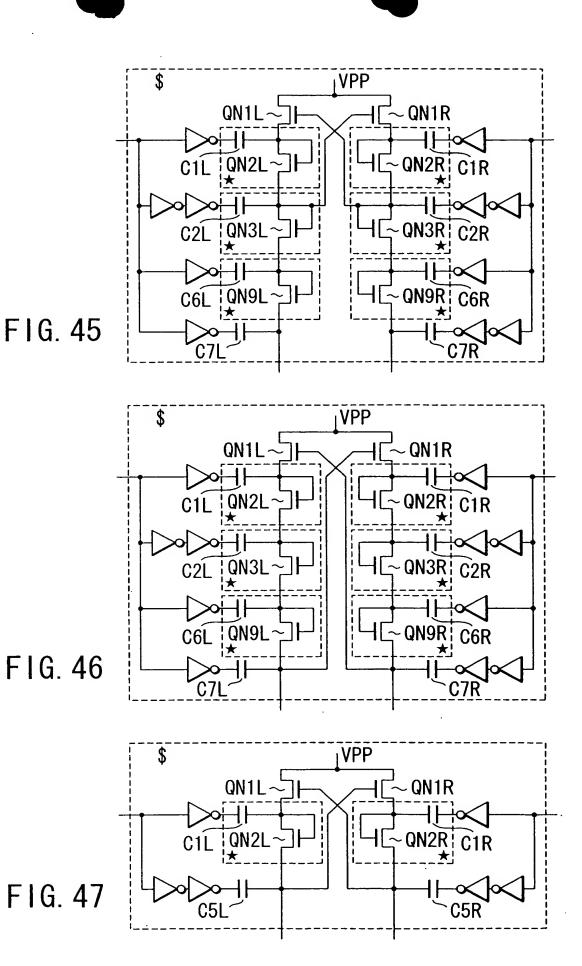


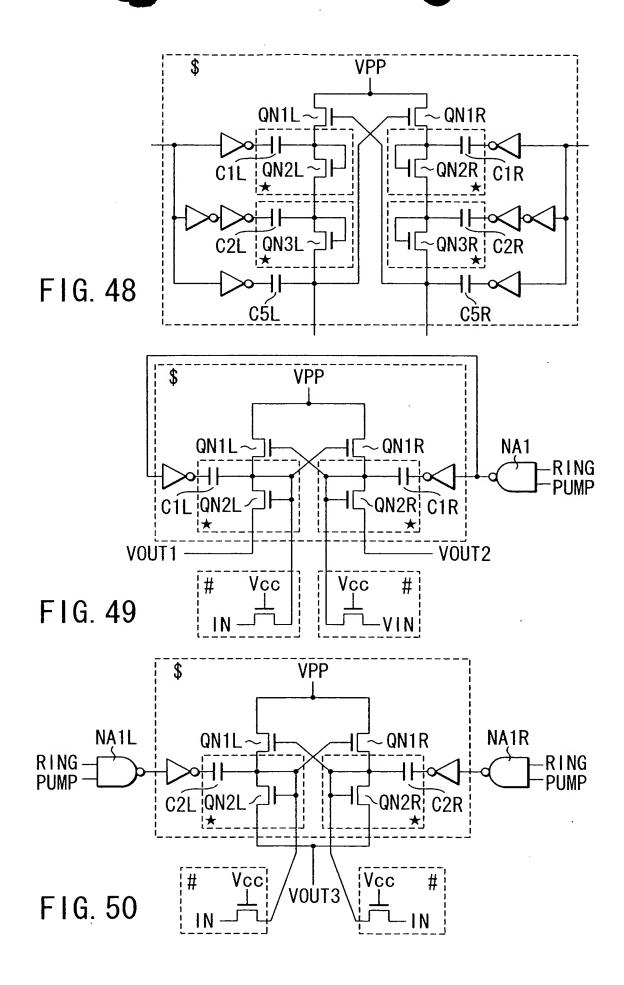








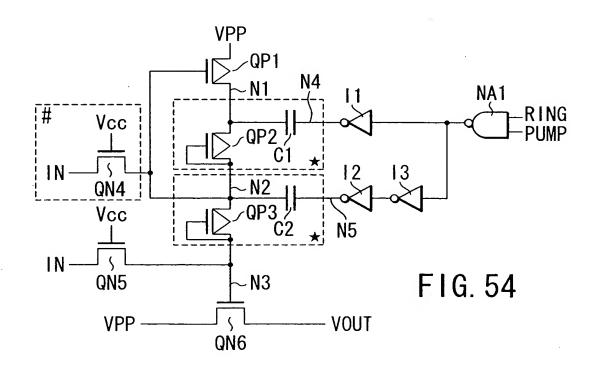




$\begin{array}{c} \text{VOUT1 (OR VOUT2 OR VOUT3)} \\ \text{FIG. 51} & \text{VPP} \begin{array}{c} \checkmark \\ \text{(OR Vcc)} \end{array} \text{ VOUT} \end{array}$

FIG.
$$52\left\{\frac{RING}{RING}\right\}$$

FIG. 53
$$\left\{\begin{array}{l} \frac{RING}{RING} \\ \end{array}\right.$$



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